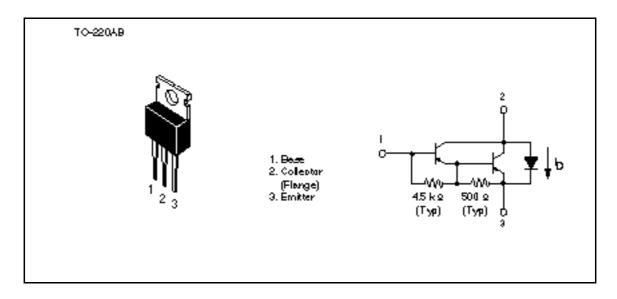
Silicon PNP Triple Diffused

HITACHI

Application

Low frequency power amplifier

Outline





Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

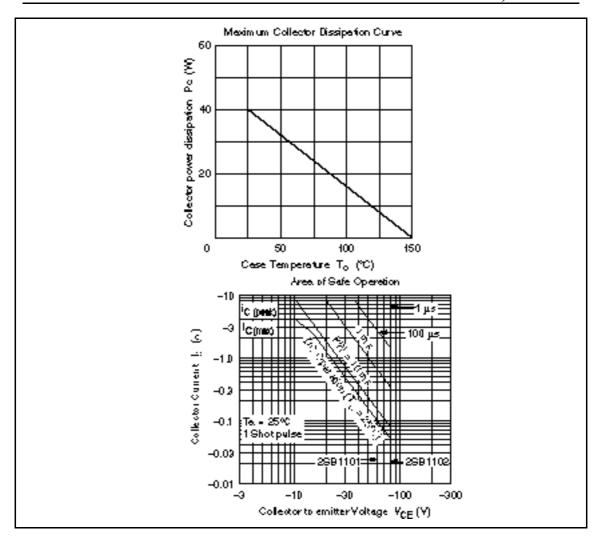
		Ratings			
Item	Symbol	2SB1101	2SB1102	Unit	
Collector to base voltage	V _{CBO}	-60	-80	V	
Collector to emitter voltage	V _{CEO}	-60	-80	V	
Emitter to base voltage	V_{EBO}	– 7	-7	V	
Collector current	I _c	-4	-4	A	
Collector peak current	I _{C(peak)}	-8	-8	A	
Collector power dissipation	P _c *1	40	40	W	
Junction temperature	Tj	150	150	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	
C to E diode forward current	I _D *1	4	4	A	

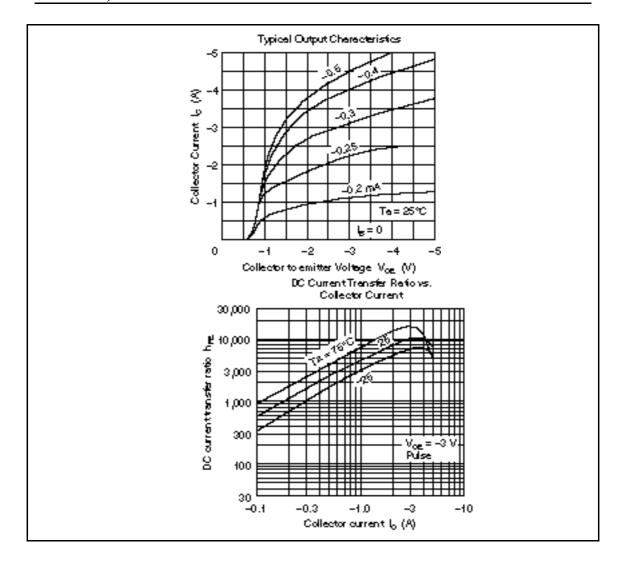
Note: 1. Value at $T_c = 25$ °C.

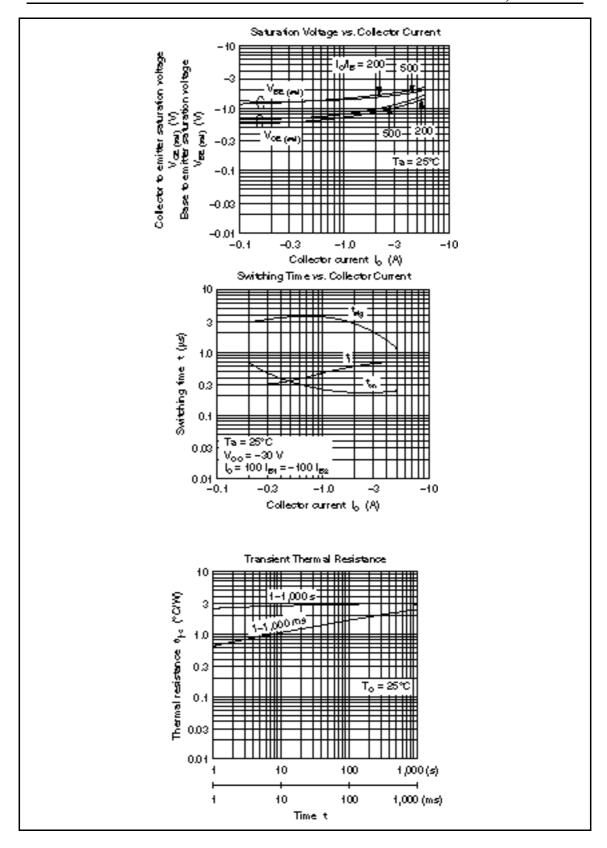
Electrical Characteristics ($Ta = 25^{\circ}C$)

		2SB1101 2SB		2SB1	SB1102				
Item	Symbol	Min	Тур	Max	Min	Тур	Max	Unit	Test conditions
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	-60	_	_	-80	_	_	V	$I_C = -25 \text{ mA}, R_{BE} =$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	-7	_	_	-7	_	_	V	$I_{\rm E} = -50 \text{ mA}, I_{\rm C} = 0$
Collector cutoff current	I _{CBO}	_	_	-100	_	_	-100	μΑ	$V_{CB} = -60 \text{ V}, I_{E} = 0$
	I _{CEO}	_	_	-10	_	_	-10	μΑ	$V_{CE} = -50 \text{ V}, R_{BE} =$
DC current tarnsfer ratio	h _{FE}	1000	_	20000	1000	_	20000		$V_{CE} = -3 \text{ V}, I_{C} = -2$ A^{*1}
Collector to emitter saturation voltage	$V_{\text{CE(sat)1}}$	_	_	-1.5	_	_	-1.5	V	$I_{\rm C} = -2 \text{ A}, I_{\rm B} = -4 \text{ mA}^{*1}$
	V _{CE(sat)2}	_	_	-3.0	_	_	-3.0	_	$I_{\rm C} = -4 \text{ A}, I_{\rm B} = -40 \text{ mA}^{*1}$
Base to emitter saturation voltage	$V_{BE(sat)1}$	_	_	-2.0	_	_	-2.0	V	$I_{\rm C} = -2 \text{ A}, I_{\rm B} = -4 \text{ mA}^{*1}$
	$V_{BE(sat)2}$	_	_	-3.5	_	_	-3.5	_	$I_{\rm C} = -4 \text{ A}, I_{\rm B} = -40 \text{ mA}^{*1}$
C to E diode forward voltage	V_{D}	_	_	3.0	_	_	3.0	V	I _D = 4 A
Turn on time	t _{on}	_	0.8	_	_	8.0	_	μs	I _C = -2 A,
Storage time	t _{stg}	_	4.0	_	_	4.0	_	_	$I_{B1} = -I_{B2} = -4 \text{ mA}$
Fall time	t _f	_	1.0	_	_	1.0	_		

Note: 1. Pulse Test.







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